L Number	Hits	Search Text	DB	Time stamp
1	8345	<pre>(micromirror\$1 or micro-mirror\$1 or (micro adj2 mirror\$1))</pre>	USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:46
2	314411	(substrate\$1 or wafer\$1 or template\$1) with silicon\$2	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:47
3	57617	circuit\$2 with CMOS	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:47
4	1390637	electrode\$1	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:47
5	489892	hinge\$1	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:47
6	193578	polygon or rectangle or hexagon	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:48
7	1646445	stop\$3	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:48
8	1617	(359/290,291,292,294,295,298,318).CCLS.	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:49
9	1949	((micromirror\$1 or micro-mirror\$1 or (micro adj2 mirror\$1))) and ((substrate\$1 or wafer\$1 or template\$1) with silicon\$2)	DERWENT USPAT; US-PGPUB; EPO; JPO;	2004/10/21 10:49
10	270	<pre>(circuit\$2 with CMOS) and (((micromirror\$1 or micro-mirror\$1 or (micro adj2 mirror\$1))) and ((substrate\$1 or wafer\$1 or template\$1) with silicon\$2))</pre>	DERWENT USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21 10:49
11	189	1 " = = " , , , ,	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21 10:50
12	125		USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21
13	18	<pre>(polygon or rectangle or hexagon) and (hinge\$1 and (electrode\$1 and ((circuit\$2 with CMOS) and (((micromirror\$1 or micro-mirror\$1 or (micro adj2 mirror\$1))) and ((substrate\$1 or wafer\$1 or</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21 10:50
14	11	<pre>template\$1) with silicon\$2))))) stop\$3 and ((polygon or rectangle or hexagon) and (hinge\$1 and (electrode\$1 and ((circuit\$2 with CMOS) and (((micromirror\$1 or micro-mirror\$1 or (micro adj2 mirror\$1))) and ((substrate\$1 or wafer\$1 or template\$1) with silicon\$2))))))</pre>	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21 10:50

15	3	and (stop\$3 and ((polygon or rectangle or hexagon) and (hinge\$1 and (electrode\$1 and ((circuit\$2 with CMOS) and (((micromirror\$1 or micro-mirror\$1 or	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/21 10:50
		<pre>(micro adj2 mirror\$1))) and ((substrate\$1 or wafer\$1 or template\$1) with silicon\$2)))))))</pre>		